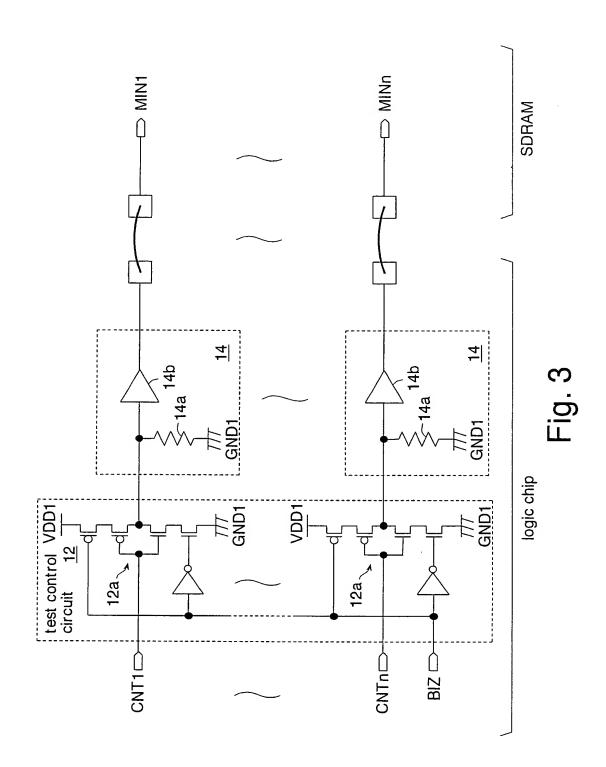
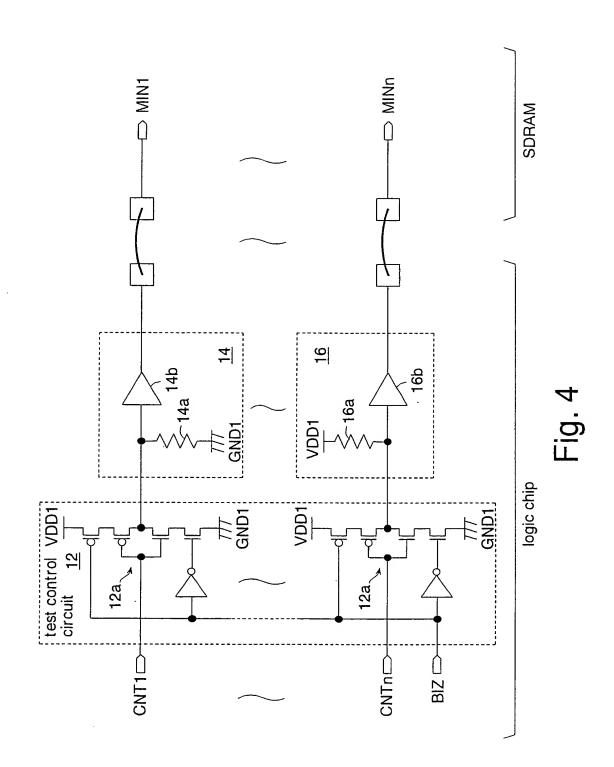
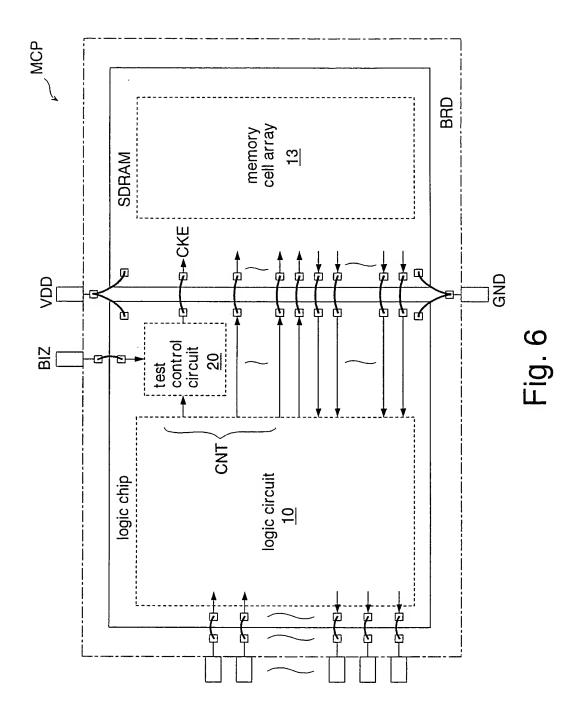


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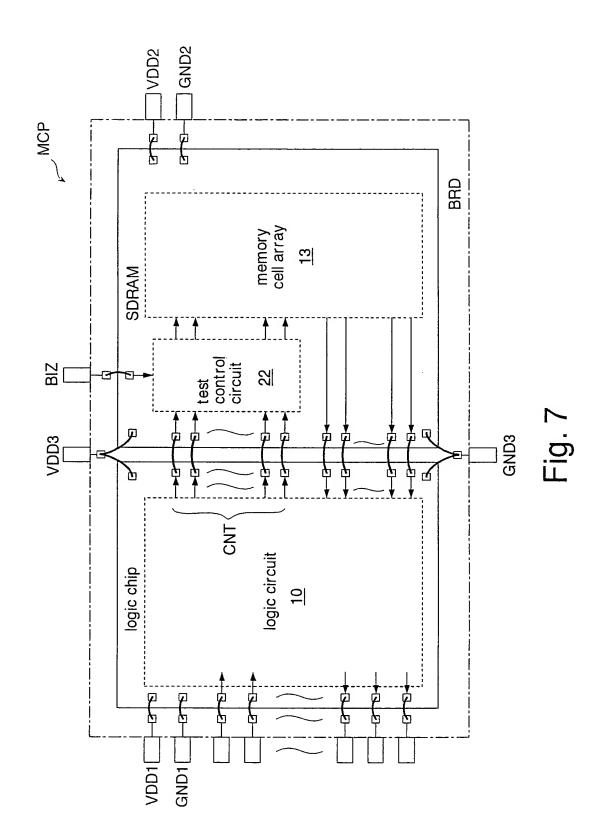




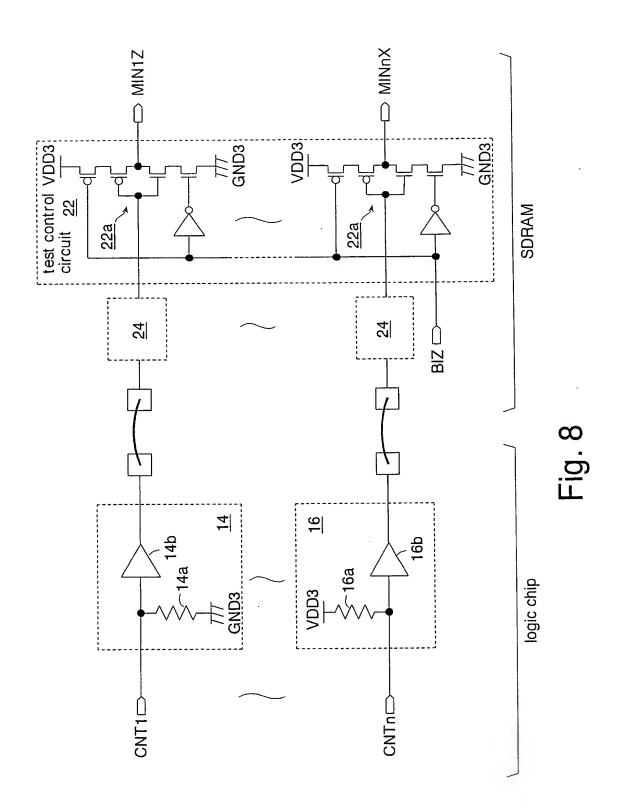
SDRAM 9 7 16b Fig. 5 ى 16a logic chip 8 GND1 18b √ VDD1 test control circuit BIZ□



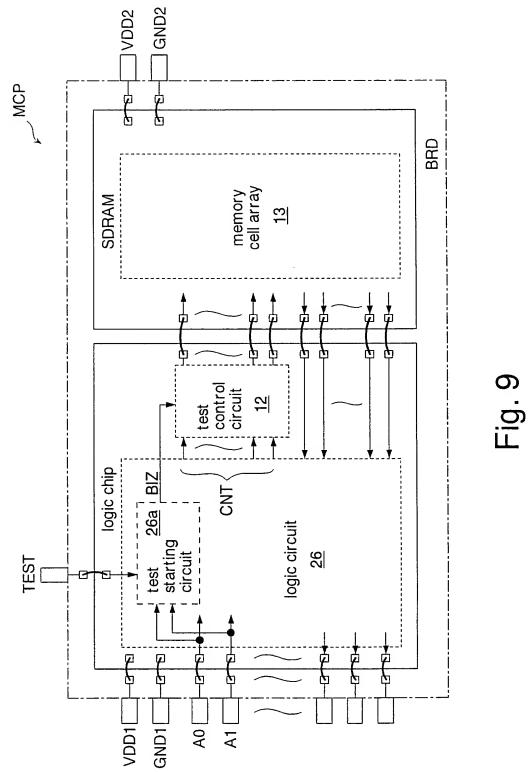
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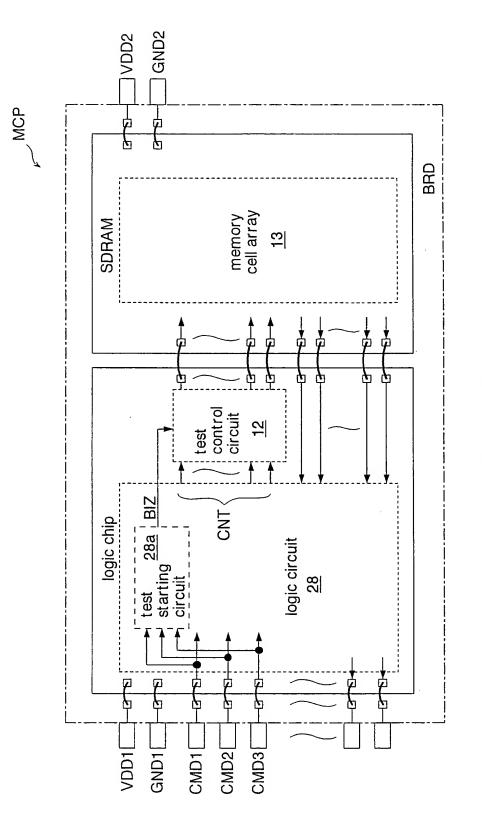


Fig. 10